



PATENT

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eric M. Dowling

Serial No.: 10/074,705

Filed: February 13, 2002

For : EMBEDDED-DRAM-DSP

**ARCHITECTURE** 

Examiner: Unknown

Art Unit: 2154

27299

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, Atm: Application Processing Division, Customer Correction Branch,

March 25, 2002 (Date)

> Robert F. Gazdziński Reg. No. 39,990

> > APR 2 9 2002

REQUEST FOR CORRECTION OF FILING RECEIPT

Assistant Commissioner for Patents Washington, D.C. 20231

ATTN:

APPLICATION PROCESSING DIVISION CUSTOMER CORRECTION BRANCH

Dear Sir:

Applicant hereby respectfully requests that the official Filing Receipt be corrected as

follows:

The Continuing Data as Claimed by Applicant should read:

THIS APPLN IS A DIVISIONAL OF 10/001,007 filed 11/14/2001 WHICH CLAIMS PRIORITY BENEFIT OF APPLN 09/021,933 FILED 02/11/1998 WHICH CLAIMS PRIORITY BENEFIT OF APPLN 60/054,439 FILED 08/01/1997

**Application No.:** 

10/074,705

Filed:

February 13, 2002

Attorney Docket No.

SEARCHP.011C1DV2

A copy of the Filing Receipt with the correction indicated in red ink is enclosed. Please forward the corrected Filing Receipt to the undersigned.

Respectfully submitted,

GAZDZINSKI & ASSOCIATES

Dated: March <u>25</u>, 2002

By:

Robert F. Gazdzinski

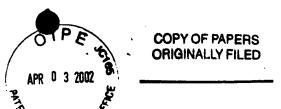
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### United States Patent and Trabemark Office

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IND CLAIMS FIL FEE REC'D ATTY.DOCKET.NO DRAWINGS TOT CLAIMS GRP ART UNIT APPLICATION NUMBER FILING DATE 2006 SEARCHP.011C1DV2 39 14 2154 02/13/2002 10/074,705

**CONFIRMATION NO. 8133** 

**FILING RECEIPT** 

\*OC000000007549946\*

**GAZDZINSKI & ASSOCIATES** Suite A232 3914 Murphy Canyon Road San Diego, CA 92123

Date Mailed: 02/28/2002

APR 2 9 2002 Technology Center 2100

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Eric M. Dowling, Richardson, TX;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A DIV OF 10/001,007 11/14/2001 WHICH CLAIMS PRIORITY BENEFIT WHICH CLAIMS BENEFIT OF 60/051 100 07

OF APPLN 09/081, 933 WHICH CLAIMS BENEFIT OF 60/054,439 08/01/1997 FILED Dalli 1898 7

Foreign Applications

If Required, Foreign Filing License Granted 02/27/2002

Projected Publication Date: 06/06/2002

Non-Publication Request: No.

Early Publication Request: No

Title

Embedded-DRAM-DSP architecture

**Preliminary Class** 

712



## LICE的S基本的常FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

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